



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Evers et al.

Serial No.: 10/616,078

Filed: July 7, 2003

For: NON-CONDUCTIVE AND SELF-
LEVELING LEADFRAME CLAMP
INSERT FOR WIREBONDING
INTEGRATED CIRCUITS

Confirmation No.: 9926

Examiner: K. Stoner

Group Art Unit: 1725

Attorney Docket No.: 2269-3081.7US
(96-1118.07/US)

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. A copy of the listed document is enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an

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admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

<u>U.S. Patent Documents</u>		
<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 5,193,733	03/16/1993	You

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed concurrently with an RCE in the above-identified application, and therefore no additional fee is due.

Respectfully submitted,



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Date: November 17, 2004

JRD/dlm:dlh

Enclosures: Form PTO-1449 or PTO/SB/08

Copy of documents cited

Document in ProLaw

